



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Kozo KIMURA et al.

Group Art Unit : Not Yet Assigned

Appl. No. : 10/599,494 (U.S. National
Stage of PCT/JP2005/006490)

Examiner : Not Yet Assigned

I.A. Filed : April 1, 2005

Confirmation No. : 9808

For : INTEGRATED CIRCUIT FOR VIDEO/AUDIO PROCESSING

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop AMENDMENT
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and §§1.97-1.98, Applicants hereby bring to the attention of the Examiner the following information.

The following documents were cited in an International Search Report with respect to International Patent Application No. PCT/JP2005/006490, of which the present application is the U.S. National Stage Application. A copy of the International Search Report has been filed concurrently with the present application and should thus have already been brought to the attention of the Examiner.

- (1) UEHARA et al., "DTV Global Platform no Kaihatsu" (Development of a DTV Global Platform), Matsushita Technical Journal, February 18, 2004, Vol. 50, No. 1, pages 2-6, together with an English language translation of the same;

- (2) European Patent Publication No. EP 1 102 169 A1, along with patent family member Japanese Laid-Open Patent Publication No. 2001-142869 A, together with an English language Abstract of the same;
- (3) U.S. Patent Application Publication No. 2003/0222877, along with patent family member Japanese Laid-Open Patent Publication No. 2004-005287 A, together with an English language Abstract of the same; and
- (4) KIMURA et al., "Software deno Jitsujikan Shori o Jitsugen shita Minseiyo Shori Processor 'Media Core Processor'" (Media Processor for Multimedia Consumer Products 'Media Core Processor'), Matsushita Technical Journal, April 18, 1999, Vol. 45, No. 2, pages 99-106, together with an English language translation of the same.

The relevance of the documents cited in the International Search Report, as ascertained with respect to the international claims by the International Examiner, is set forth in the International Search Report.

Applicants further bring to the attention of the Examiner the following documents.

- (5) Texas Instruments Incorporated, "TMS320DM642, Video/Imaging Fixed-Point Digital Signal Processor," page 4. Applicants note that this document is cited beginning at page 1 of the specification of the above-captioned application; and
- (6) Japanese Laid-Open Patent Publication No. HEI 10-275135 A, together with an English language Abstract of the same, and patent family member U.S. Patent No. 5,977,997.

Further to 37 C.F.R. §1.98 (a)(2)(ii), copies of the U.S. patent publications are not enclosed herewith. However, if any copy is needed, the Examiner is respectfully requested to

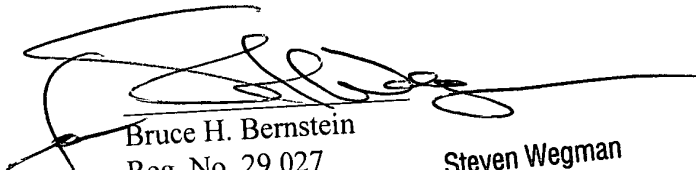
P30890. A01

contact the undersigned.

Applicants respectfully request that the Examiner consider the above material and cite the same. Copies of the above-noted documents, with the exception of the U.S. patent publications, are attached hereto, and all of the documents are listed on an attached PTO-1449 Form. The Examiner is requested to initial the appropriate spaces on the attached Form and to return a copy of the completed Form to Applicants with the next official communication in the present application.

Should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,
Kozo KIMURA et al.


Bruce H. Bernstein
Reg. No. 29,027

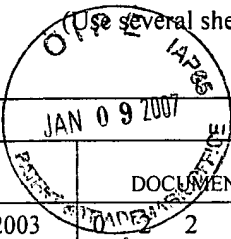
Steven Wegman
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January 9, 2007
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FORM PTO-1449

U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
P30890Application No.
10/599,494INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(Use several sheets if necessary)

Applicant
Kozo KIMURA et al.Filing Date
April 1, 2005Group
Unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
2003	2 2 8 7 7	12/04/03	TANAKA et al.			
	5 9 7 7 9 9 7	11/02/99	VAINSENER			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	1 1 0 2 1 6 9	05/23/01	E.P.O			
2001	- 1 4 2 8 6 9	05/25/01	JAPAN			
2004	- 0 0 5 2 8 7	01/08/04	JAPAN			
10	- 2 7 5 1 3 5	10/13/98	JAPAN			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

1	UEHARA et al., "DTV Global Platform no Kaihatsu" (Development of a DTV Global Platform), Matsushita Technical Journal, February 18, 2004, Vol. 50, No. 1, pages 2-6, together with an English language translation of the same.
2	English language Abstract of JP 2001-142869.
3	English language Abstract of JP 2004-005287.
4	English language Abstract of JP 10-275135.
5	KIMURA et al., "Software deno Jitsujikan Shori o Jitsugen shita Minseiyo Shori Processor 'Media Core Processor'" (Media Processor for Multimedia Consumer Products 'Media Core Processor'), Matsushita Technical Journal, April 18, 1999, Vol. 45, No. 2, pages 99-106, together with an English language translation of the same.
6	Texas Instruments Incorporated, "TMS320DM642, Video/Imaging Fixed-Point Digital Signal Processor," page 4.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.